

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA KAKINADA – 533 003, Andhra Pradesh, India

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE STRUCTURE AND SYLLABUS

For **UG** – **R20**

B. TECH - ELECTRONICS AND COMMUNICATION ENGINEERING

(Applicable for batches admitted from 2020-2021)



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA KAKINADA - 533 003, ANDHRA PRADESH, INDIA



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE STRUCTURE

I Year –I SEMESTER

S. No.	Category	Subjects L T P		P	Credits	
1	HS	Communicative English	3	0	0	3
2	BS	Mathematics –I(Calculus)	3	0	0	3
3	BS	Applied Chemistry	3	0	0	3
4	ES	Programming for Problem Solving Using C	3	0	0	3
5	BS	Engineering Drawing	2	0	2	3
6	LC	English Communication Skills Laboratory	0	0	3	1.5
7	LC	Applied Chemistry Lab	0	0	3	1.5
8	LC	Programming for Problem Solving Using C Lab	0	0	3	1.5
Total Credits						

I Year - II SEMESTER

S. No	Category	Subjects	L	Т	P	Credits
1	BS	Mathematics –II (Linear Algebra and Numerical Methods)	3	0	0	3
2	BS	Applied Physics	3	0	0	3
3	ES	Object Oriented Programming through Java	2	0	2	3
4	ES	Network Analysis	3	0	0	3
5	ES	Basic Electrical Engineering	3	0	0	3
6	LC	Electronic workshop Lab	0	0	3	1.5
7	LC	Basic Electrical Engineering Lab	0	0	3	1.5
8	LC	Applied Physics Lab	0	0	3	1.5
9	MC	Environmental Science	3	0	0	0.0
Total Credits						



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II Year –I Semester

S. No	Category	Name of the Subject	L	Т	P	Credits	
1	PC	Electronic Devices and Circuits	3	1	0	3	
2	PC	Switching Theory and Logic Design	3	1	0	3	
3	PC	Signals and Systems	3	1	0	3	
4	BS	Mathematics-III (Transforms and Vector Calculus)	3	1	0	3	
5	BS	Random Variables and Stochastic Processes	3	1	0	3	
6	LC	OOPS through Java Lab	0	0	2	1.5	
7	LC	Electronic Devices and Circuits -Lab	0	0	2	1.5	
8	LC	Switching Theory and Logic Design-Lab	0	0	2	1.5	
9	SC	Python Programming	0	0	4	2	
Total Credits							

II Year – II Semester

S. No	Category	Name of the subject	L	Т	P	Credits	
1	PC	Electronic Circuit Analysis 3 1 0					
2	PC	Digital IC Design	3	1	0	3	
3	PC	Analog Communications	3	0	0	3	
4	ES	Linear control Systems	3	1	0	3	
5	HS	Management and Organizational Behavior	3	0	0	3	
6	LC	Electronic Circuit Analysis Lab	0	0	3	1.5	
7	LC	Analog Communications Lab	0	0	3	1.5	
8	LC	Digital IC Design Lab	0	0	3	1.5	
9	SC	Soft Skills	0	0	4	2	
10	MC	Constitution of India	3	3 0 0		0	
Total Credits							
Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)							



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III Year - I Semester

S. No	Category	Name of the subject	L	L T P		Credits			
1	PC	Analog ICs and Applications	3	0	0	3			
2	PC	Electromagnetic Waves and Transmission Lines	3	0	0	3			
3	PC	Digital Communications	3	0	0	3			
4	OE1	Open Elective Course/Job oriented elective-1	2	0	2	3			
5	PE1	Professional Elective courses -1	3	0	0	3			
6	LC	Analog ICs and Applications LAB	0	0	3	1.5			
7	LC	Digital Communications Lab	0	0	3	1.5			
8	SC	Data Structures using Java Lab	0	0	4	2			
9	MC	Indian Traditional Knowledge	2	0	0	0			
	Summer	0	1.5						
			T	otal cı	redits	21.5			
	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)								

<u>PE1:</u>	OE1:
Antenna and Wave Propagation Electronic Measurements and Instrumentation	Candidate should select the subject from list of subjects offered by other
3. Computer Architecture & Organization	departments



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III Year -II Semester

S. No	Category	Name of the subject	L	Т	P	Credits		
1	PC	Microprocessor and Microcontrollers		1	0	3		
2	PC	VLSI Design	3	0	0	3		
3	PC	Digital Signal Processing	3	0	0	3		
4	PE2	Professional Elective courses - 2	3	0 0		3		
5	OE 2	Open Elective Course/Job oriented elective -2	2	0	2	3		
6	LC	Microprocessor and Microcontrollers - Lab	0	0	3	1.5		
7	LC	VLSI Design Lab	0	0	3	1.5		
8	LC	Digital Signal Processing Lab	0	0	3	1.5		
9	SC	ARM based/ Aurdino based Programming	1	0	2	2		
10	MC	Research Methodology	2	2 0 0		0		
	Total credits							
Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)								

Industrial/Research Internship (Mandatory) 2 Months during summer vacation

<u>PE2:</u>	OE2:
1. Microwave Engineering 2. Mobile & Cellular Communication 3. Embedded Systems 4. CMOS Analog IC Design	Candidate should select the subject from list of subjects offered by other departments



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING IV Year –I Semester

S. No	Category	Name of the subject	L	T	P	Credits	
1	PE	Professional Elective courses -3	3	0	0	3	
2	PE	Professional Elective courses -4	3	0	0	3	
3	PE	Professional Elective courses -5 3 0		0	3		
4	OE	Open Elective Courses/ Job oriented elective -3	2	0	2	3	
5	OE	Open Elective Courses/ Job oriented elective -4	2	0	2	3	
6	HS	*Humanities and Social Science Elective	3	0	0	3	
7	SC	Designer tools (HFSS, Microwave Studio CST. Cadence Virtuoso. Synopsys, Mentor Graphics, Xilinx.)	1	0	2	2	
Industrial/Research Internship 2 Months (Mandatory) afterthird year (to be evaluated during VII semester 0 0 0						3	
	Total credits						
	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)						

<u>PE 3:</u>	<u>PE5:</u>
1. Optical Communication 2. Digital Image Processing 3. Low Power VLSI Design	1. Radar engineering 2.Pattern recognition & Machine Learning 3.Internet of Things
<u>PE4:</u>	
1.Satellite Communications 2.Soft Computing Techniques 3.Digital IC Design using CMOS	

IV Year – II Semester

S. No.	Category	Code	Course Title	Hours per week		Credits			
1	Major Project	PROJ	Project work, seminar and internship inindustry	-	-	-	12		
	INTERNSHIP (6 MONTHS)								
Total credits							12		