

ADITYA ENGINEERING COLLEGE (A)

ADB Road, Surampalem

SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

II FORTNIGHT OF FEBRUARY-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 28-02-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	Unit-1 50% completed	Unit-1 60% to be completed	Sources of IOT	-1	More explanation	P.B. Babu
2	VLSI Design	Dr. Suresh Gulivindala	Unit-1 60% completed	Unit-1 60% to be completed	—	—	—	Dr. Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	Unit-I 40% completed	Unit-I 60% to be completed	Discrete Time Signals	-2	more explanation	Dr. Sridevi
4	Embedded Systems	Dr. Yashu Swami	Unit-I 50% completed	Unit-I 60% to be completed	Sensors and Actuators	-1	more explanation	Dr. Yashu
5	Radar Systems	Dr. G.Vishnu Vardhan	Unit-1 60% completed	Unit-1 60% to be completed	—	—	—	Dr. Vishnu
6	Management Science	Mrs. V.Suneetha	Unit-1 60% completed	Unit-1 60% to be completed	—	—	—	Mrs. Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms.K.Divya Madhuri	Cycle - I 3 Expt completed	Cycle - I	Realization of logic gates	—	—	Dr. Yashu
8	Internet of Things Lab	Mr. P.Bujji Babu/ Dr. Santoshkumar Sabat	Cycle - I Exp-1 Completed	Cycle - I	Active & Passive Sensors	—	—	Dr. Santosh

Signature of HOD
Head of the Department
Department of E.C.E.

PRINCIPAL
ADITYA ENGINEERING COLLEGE
SURAMPALAM - 533 437

Dean (Academics)
Aditya Engineering College (A)
Surampalem

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ADB Road, Surampalem

SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

II FORTNIGHT OF FEBRUARY-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 28-02-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	Unit-1 50% completed	Unit-1 60% to be completed	Sources of IOT	-1	More explanation	P.B. Babu
2	VLSI Design	Dr. Suresh Gulivindala	Unit-1 60% completed	Unit-1 60% to be completed	-	-	-	Dr. Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	Unit-I 40% completed	Unit-I 60% to be completed	Discrete Time Signals and Systems	-2	more explanation	Dr. Sridevi
4	Embedded Systems	Dr. Yashu Swami	Unit-I 50% completed	Unit-I 60% to be completed	Sensors and Actuators	-1	more explanation	Dr. Yashu
5	Radar Systems	Dr. G. Vishnu Vardhan	Unit-1 60% completed	Unit-I 60% to be completed	-	-	-	V. Vardhan
6	Management Science	Mrs. V. Suneetha	Unit-1 60% completed	Unit-1 60% to be completed	-	-	-	V. Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms. K. Divya Madhuri	Cycle-1 3 Expt completed	Cycle-I	Realization of logic gates	-	-	Yashu
8	Internet of Things Lab	Mr. P.Bujji Babu/ Dr. Santoshkumar Sabat	Cycle-1 Exp-1 Completed	Cycle-1	Active & Passive Sensors	-	-	Santosh

Signature of HOD
Head of the Department
Department of E.C.E.
Aditya Engineering College (A9)

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SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

I FORTNIGHT OF MARCH-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 15-03-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	Unit-2 20% completed	Unit-II 30% to be completed	Arduino libraries	-1	More holidays	P.Bujji
2	VLSI Design	Dr. Suresh Gulivindala	Unit-2 20% completed	Unit-2 20% to be completed	Thresold voltage	-1	more explanation	G. Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	Unit-2 30% completed	Unit-2 30% to be completed	-	-	-	G. Sridevi
4	Embedded Systems	Dr. Yashu Swami	Unit-2 30% Completed	Unit-2 30% to be completed	-	-	-	Y. Swami
5	Radar Systems	Dr. G.Vishnu Vardhan	Unit-2 30% Completed	Unit-2 30% to be completed	-	-	-	V. Vardhan
6	Management Science	Mrs. V.Suneetha	Unit-2 20% Completed	Unit-2 30% to be completed	Control charts	-1	more explanation	V. Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms.K.Divya Madhuri	Cycle-1 4 expts completed	Cycle-1	3:8 decoder	-	-	Yashu
8	Internet of Things Lab	Mr. P.Bujji Babu/ Dr. Santoshkumar Sabat	Cycle-1, 3 Experiments Completed	Cycle-1	Interfacing of analog sensors	-	-	Santosh

Signature of Head of Department
Department of E.C.E.
Aditya Engineering College (A3)

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SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

II FORTNIGHT OF MARCH-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 31-03-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	unit-III 20% to be completed	unit-III 40% to be completed	Actuators	- 2	More holidays	P.Babu
2	VLSI Design	Dr. Suresh Gulivindala	unit-III 30% completed	unit-III 40% to be completed	Ri-CMOS rules	- 1	more explanation	Dr. Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	unit-3 20% completed	unit-3 40% to be completed	chebyshev filter	- 2	more explanation	Dr. Sridevi
4	Embedded Systems	Dr. Yashu Swami	unit-3 40% completed	unit-3 40% to be completed	-	-	-	Y. Swami
5	Radar Systems	Dr. G.Vishnu Vardhan	unit-3 30% completed	unit-3 40% to be completed	filter character - slice	- 1	more explanation	V. Vardhan
6	Management Science	Mrs. V.Suneetha	unit-3 30% completed	unit-3 40% to be completed	wage payment - Flows	- 1	more holidays	V. Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms.K.Divya Madhuri	CYCLE-1 5-expts completed	CYCLE-1	decode counter	-	-	Y. Swami
8	Internet of Things Lab	Mr. P.Bujji Babu/ Dr. Santoshkumar Sabat	Cycle-1 5 expts Completed	Cycle-1	actuators	-	-	S. Sabat

Signature of HOD
Head of the Department
Department of E.C.E.

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ADB Road, Surampalem

SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

1 FORTNIGHT OF APRIL-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 15-04-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	Unit - I Completed	Unit - IV 20% to be completed	Cloud platforms	- 2	More explanation	P. Bujji
2	VLSI Design	Dr. Suresh Gulivindala	Unit - III Completed	Unit - IV 20% to be completed	sheet resistance	- 2	more holidays	S. Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	Unit - 3 30% Completed	Unit - 4 50% 20% to be completed	FIR filter	- 2	more holidays	G. Sridevi
4	Embedded Systems	Dr. Yashu Swami	Unit - 3 30% Completed	Unit - 4 20% to be completed	operating system basics	- 2	more explanation	Y. Swami
5	Radar Systems	Dr. G. Vishnu Vardhan	Unit - 3 Completed	Unit - 4 20% to be completed	Conical Scan	- 2	more holidays	V. Vardhan
6	Management Science	Mrs. V. Suneetha	Unit - 4 20% Completed	Unit - 4 20% to be completed	-	-	-	V. Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms. K. Divya Madhuri	Cycle - I Introduction	Cycle - I	Introduction	-	-	Yashu
8	Internet of Things Lab	Mr. P. Bujji Babu/ Dr. Santosh Kumar Sabat	Cycle - 2 Expts Completed	Cycle - 2	Controller actuator	-	-	Santosh

Head of the Department
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ADB Road, Surampalem

SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

II FORTNIGHT OF APRIL-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 30-04-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	unit-IV 80% completed	unit-V 10% to be completed	IOT Application	-1	More explanation	P.Bu
2	VLSI Design	Dr. Suresh Gulivindala	unit-IV 70% completed	unit-V 10% to be completed	VLSI design issues	-2	more holidays	G. Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	unit-4 80% completed	unit-5 10% to be completed	Interpolation	-1	more holidays	G. Sridevi
4	Embedded Systems	Dr. Yashu Swami	unit-4 completed	unit-5 10% to be completed	IDE.	-1	more explanation	Y. Swami
5	Radar Systems	Dr. G. Vishnu Vardhan	unit-4 completed	unit-5 10% to be completed	detection of radar signals in noise	-1	more holidays	V. Vardhan
6	Management Science	Mrs. V. Suneetha	unit-4 80% completed	unit-5 10% to be completed	Goals & strategies	-3	more holidays	V. Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms. K. Divya Madhuri	Cycle-1 1-exp. completed	Cycle-1	Inverter	-	-	Y. Swami
8	Internet of Things Lab	Mr. P. Bujji Babu/ Dr. Santoshkumar Sabat	Cycle-2 4 expt Completed	Cycle-2	Cloud platform	-	-	Santosh

Head of the Department
Signature of HOD
Department of E.C.E.
Aditya Engineering College (A9)

Principal
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ADITYA ENGINEERING COLLEGE (A)

ADB Road, Surampalem

SYLLABUS STATUS REPORT (2021-22) EVEN SEMESTER

I FORTNIGHT OF MAY-2022

Year : VI SEM ECE-A

Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Date: 14-05-2022

S.No	Name of the Course	Name of the Faculty	Syllabus Covered as on date (No. of Units Covered)	Actual Syllabus to be Covered (As per lesson Plan)	Topic Last Covered	No. of Classes in Leading / Lagging (+) / (-)	Remarks (Reasons for not Covering in time as per Lessonplan)	Signature
1	Internet of Things	Mr. P.Bujji Babu	unit-5 60% completed	unit-5 80% to be completed	Smart Agriculture	-2	more explanation	P.Bujji
2	VLSI Design	Dr. Suresh Gulivindala	unit-5 60% completed	unit-5 80% to be completed	FPGA Design Flow	-2	more explanation	Suresh
3	Digital Signal Processing	Dr. Sridevi Gamini	unit-5 70% completed	unit-5 80% to be completed	Trans-multiplexer	-1	more explanation	Gamini
4	Embedded Systems	Dr. Yashu Swami	unit-5 50% completed	unit-5 80% to be completed	CAD and hardware	-3	more holidays	Yashu
5	Radar Systems	Dr. G.Vishnu Vardhan	unit-5 70% completed	unit-5 80% to be completed	Radiation Pattern	-2	more explanation	V.Vardhan
6	Management Science	Mrs. V.Suneetha	unit-5 80% completed	unit-5 80% to be completed	-	-	-	V.Suneetha
7	VLSI Lab	Dr. Yashu Swami/ Ms.K.Divya Madhuri	cycle-1 3 exp completed	cycle-1	Full Adder	-	-	Yashu
8	Internet of Things Lab	Mr. P.Bujji Babu/ Dr. Santoshkumar Sabat	Cycle-2 4 exp Completed	Cycle-2	IoT Prototypes	-	-	Santosh

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