

ADITYA ENGINEERING COLLEGE

An Autonomous Institution

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Recognised by UGC under sections 2(f) and 12(B) of UGC Act, 1956

Aditya Nagar, ADB Road, Surampalem - 533437, Near Kakinada, E.G.Dt., Ph:99498 76662

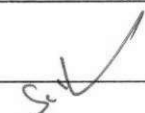
Program Name : B.Tech. in Electrical and Electronics Engineering

Syllabus Revision for the Academic Year 2017-2018

S.No	Semester	Course Code	Course Name	% of content revised for the existing year
1	I	171HS1T01	English - I	55
2	I	171BS1T01	Mathematics - I	40
3	I	171HS1T02	Environmental Studies	20
4	I	171BS1T05	Applied Chemistry	20
5	I	171ES1T02	Engineering Mechanics	0
6	I	171ES1T01	Computer Programming	0
7	I	171HS1L01	English Communication Skills Lab - I	10
8	I	171BS1L03	Applied Chemistry Lab	0
9	I	171ES1L01	Computer Programming Lab	40
10	II	171HS2T03	English - II	65
11	II	171BS2T02	Mathematics - II	10
12	II	171BS2T06	Mathematics - III	40
13	II	171BS2T04	Applied Physics	20
14	II	171ES2T03	Engineering Drawing	5
15	II	171EE2T01	Electrical Circuit Analysis - I	5
16	II	171HS2L02	English Communication Skills Lab - II	40
17	II	171BS2L04	Applied Physics Lab	10
18	II	171ES2L02	Engineering Workshop and IT Workshop	15

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S.No	Semester	Course Code	Course Name	% of content revised for the existing year
19	III	R1621021	Electrical Circuit Analysis - II	0
20	III	R1621022	Electrical Machines-I	80
21	III	R1621023	Basic Electronics and Devices	0
22	III	R1621024	Electro Magnetic Fields	0
23	III	R1621025	Thermal and Hydro Prime Movers	0
24	III	R1621026	Managerial Economics & Financial Analysis	0
25	III	R1621027	Thermal and Hydro Laboratory	0
26	III	R1621028	Electrical Circuits Laboratory	16
27	IV	R1622021	Electrical Measurements	0
28	IV	R1622022	Electrical Machines-II	65
29	IV	R1622023	Switching Theory and Logic Design	0
30	IV	R1622024	Control Systems	0
31	IV	R1622025	Power Systems-I	0
32	IV	R1622026	Management Science	0
33	IV	R1622027	Electrical Machines -I Laboratory	60
34	IV	R1622028	Electronic Devices & Circuits Laboratory	0
35	V	RT31022	Managerial Economics and Financial Analysis	0
36	V	RT31021	Electrical Measurements	0
37	V	RT31023	Power Systems-II	0
38	V	RT31024	Electrical Machines-III	0
39	V	RT31025	Power Electronics	0
40	V	RT31026	Linear & Digital IC Applications	0



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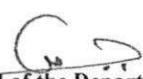
S.No	Semester	Course Code	Course Name	% of content revised for the existing year
41	V	RT31027	Electrical Machines-II Lab	0
42	V	RT31028	Control Systems Lab	0
43	V	RT31016	IPR & Patents	0
44	VI	RT32022	Switchgear and Protection	0
45	VI	RT32021	Microprocessors & Microcontrollers	0
46	VI	RT32023	Utilization of Electrical Energy	0
47	VI	RT32024	Power System Analysis	0
48	VI	RT32026	Power Semiconductor Drives	0
49	VI	RT32025	Management Science	0
50	VI	RT32027	Power Electronics Lab	0
51	VI	RT32028	Electrical Measurements Lab	0
52	VII	RT41021	Renewable Energy Sources and Systems	0
53	VII	RT41022	HVAC & DC Transmission	0
54	VII	RT41023	Power System Operation & Control	0
55	VII	RT41024	Energy Audit, Conservation and Management	0
56	VII	RT41025	Instrumentation	0
57	VII	RT41026	Non Conventional Sources of Energy	0
58	VII	RT41027	Optimization Techniques	0
59	VII	RT41028	VLSI Design	0
60	VII	RT41029	Electrical Distribution Systems	0
61	VII	RT41030	Optimization Techniques	0
62	VII	RT4102L	Microprocessors & Microcontrollers Lab	0

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S.No	Semester	Course Code	Course Name	% of content revised for the existing year
64	VII	RT4102N	Power systems lab	0
65	VIII	RT42021	Digital Control Systems	0
66	VIII	RT42022A	Advanced Control Systems	0
67	VIII	RT42022B	Extra High Voltage Transmission	0
68	VIII	RT42022C	Special Electrical Machines	0
69	VIII	RT42023A	Electric Power Quality	0
70	VIII	RT42023B	Digital Signal Processing	0
71	VIII	RT42023C	FACTS: Flexible Alternating Current Transmission System	0
72	VIII	RT42024A	OOPS Through Java	0
73	VIII	RT42024B	UNIX and Shell Programming	0
74	VIII	RT42024C	AI Techniques	0
75	VIII	RT42024D	Power System Reforms	0
76	VIII	RT42024E	Systems Engineering	0
77	VIII	RT42025	Project	0
Total number of courses in the academic year 2017-2018				= 77
Number of courses having revision in syllabus content $\geq 20\%$ in the academic year 2017-2018				= 12
Percentage of syllabus revision carried out in the academic year 2017-2018 = $(12/77) \times 100$				= 15.58%


 Program Coordinator


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 Head of the Department
 Head of The Department
 Dept: Of Electrical & Electronics Engineering
 Aditya Engineering College (A9)



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Department of Electrical and Electronics Engineering

Date: 30-05-2017

Minutes of the I meeting of BOS scheduled on 29-05-2017

The I meeting of the BOS of EEE was held on 29-05-2017 at 10.00 AM, Ajivika Conference Hall, Bill Gates Bhavan. Dr. V. Srinivasa Rao, Chairperson presided over the meeting.

Agenda 1.1: Welcome address by Chairperson

Dr. V. Srinivasa Rao, BOS Chairperson invited the distinguished members of BOS to the I BOS Meeting.

Agenda 1.2: Discussion and ratification of the Vision and Mission of the department and Program Educational Objectives (PEOs), Program Outcomes (POs) and Program Specific Outcomes (PSOs) of the Programs under the Department.

Members of BOS ratified the Vision and Mission of the department, PEOs, POs and PSOs of the Programs under the Department.

Agenda 1.3: Discussion on proposed Program Structures of AR17 B. Tech (EEE) & AR17 M. Tech (PED) and ratification of the same.

The members of BOS ratified the AR17 B. Tech (EEE) First Year Program structure and AR17 M. Tech (PED) Program structure.

Agenda 1.4: Discussion on proposed AR17 B. Tech (EEE) & AR17 M. Tech (PED) Programs – I & II semesters syllabus and ratification of the same.

BOS members approved AR 17 B.Tech (EEE) First Year course syllabus of Electrical Circuit Analysis-I, Basic Electrical and Electronics Engineering and AR17 M. Tech (PED) courses. The percentage of courses revised in this academic year 2017-2018 for B. Tech (EEE) is 15.58% and M. Tech (PED) is 14.81%. The list of courses revised is enclosed as Annexure I.

BOS members discussed the courses in the program structure which enhance the changes of getting placements for the students.

Agenda 1.5: Ratification of the proposed model question paper for sessional and external examinations of AR 17 B. Tech (EEE) and AR17 M. Tech (PED).

BOS members have approved the pattern of the model question paper for the internal and external examinations.

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Agenda 1.6: Finalization of names of reputed institution for setting question paper and valuation of answer scripts.

BOS members accepted the list consisting of the names of reputed autonomous institution for setting question paper and valuation of answer scripts.

Agenda 1.7: Discussion on Analysis of Results

The BOS Chairperson presented the odd and even semesters pass percentage for the A.Y.2016-2017. The BOS members noted the same.

Agenda 1.8: Analysis of student feedback and action taken report

BOS Chairperson expressed that the student feedback & action taken report process initiated at the end of each semester. The BOS members noted the same.

Agenda 1.9: Analysis of Stakeholder's Feedback on Curriculum.

The BOS Chairperson presented the analysis report of Stakeholder's feedback on curriculum. The BOS members noted the same and the Action Taken Report is enclosed as Annexure-II.

Agenda 1.10: Any other item/s

The following points were discussed.

- Dr. D. Suryanarayana expressed to include latest edition of textbooks.
- Dr. R. Srinivasa Rao suggested to remove topics that are repeated in other courses.
- Dr. Sivakumar K expressed that the practical oriented learning can be implemented, students can able to solve the industrial problems in all subjects.
- Dr. Sivakumar K expressed that the web links provided at the end of every course can have the content of that particular link.
- Sri Sunny John Vergheese expressed that the syllabus should be more employability specific.
- Mr. P. S. M. K. Reddy expressed that some recent coding problems to be introduced in the subjects.


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Annexure-I

List of Courses Revised in the Academic Year 2017-2018

S. No	Program	Semester	Course Code	Course Name
1	B. Tech (EEE)	I	171HS1T01	English – I
2	B. Tech (EEE)	I	171BS1T01	Mathematics - I
3	B. Tech (EEE)	I	171HS1T02	Environmental Studies
4	B. Tech (EEE)	I	171BS1T05	Applied Chemistry
5	B. Tech (EEE)	I	171ES1L01	Computer Programming Lab
6	B. Tech (EEE)	II	171HS2T03	English - II
7	B. Tech (EEE)	II	171BS2T06	Mathematics - III
8	B. Tech (EEE)	II	171BS2T04	Applied Physics
9	B. Tech (EEE)	II	171HS2L02	English Communication Skills Lab - II
10	B. Tech (EEE)	III	R1621022	Electrical Machines-I
11	B. Tech (EEE)	IV	R1622022	Electrical Machines-II
12	B. Tech (EEE)	IV	R1622027	Electrical Machines -I Laboratory
13	M. Tech (PED)	I	172PD1E01	Modern Control Theory
14	M. Tech (PED)	I	172PD1E03	Optimization Techniques
15	M. Tech (PED)	I	172PD1E05	Artificial Intelligence Techniques
16	M. Tech (PED)	I	172PD1E06	HVDC Transmission

Sub

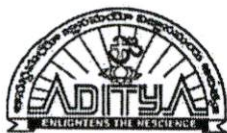
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Sub
BOS Chairperson

Head of The Department

Dept: Of Electrical & Electronics Engineering
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Annexure-II

Action Taken Report on Stakeholders Feedback in the Academic Year 2017-2018

S. No	Agenda Item No.	Stakeholders Recommended	Action Taken
1.	2.3	Students should possess the practical knowledge regarding the programming in Electrical concepts to acquire a job in core sectors	The detailed analysis of programming Electrical subjects will be effectively demonstrated with PPTs, online/offline classes considering video demonstration.
2.	1.10	New course is to be designed for the students, in order to gain the knowledge on employability skill development.	Considering employer feedback, a new course named "employability / entrepreneurship / skill development" will be included in the AR17 curriculum.
3.	2.6	The experiments on Electrical Software are to be included.	Modifications are to be made in the curriculum and the experiments on Electrical Software will be included.
4.	1.10	Industrial visits are to be conducted for the students so that they can have practical view of power stations.	Considering Employer feedback, industrial visits will be organized.
5.	1.10	Assist the students with various software and value added courses that will be beneficial to students and enhances the chances of coding skills and employability.	Various software and value added courses like problem solving using Python, application of Arduino programming, Machine learning in Electrical Engineering will be introduced.
6.	2.3	Courses are to be added to the curriculum so that, students can be aware of the electrical labs.	Course named "Electrical Safety" will be added in the curriculum that provides necessary information among safety of electrical equipment's.
7.	2.6	The syllabus on power electronics has to be reduced, because it is taking lot of time for the syllabus completion.	Considering Alumni feedback, the topic on dual converters is deducted and will be modified.

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8.	1.4	Courses are to be revised that enhances the chances of employability	The course structure of AR 17 M. Tech (PED) will be revised and modified.
9.	2.6	It would help the understudies by a large assuming that they have more options in electives where they can decide on their own decisions.	The curriculum will be modified and includes professional electives and open electives to support this recommendation.
10.	1.4	Value added courses are to be included in the curriculum, so that students can gain knowledge among the software courses and coding.	Necessary measures will be taken among the students so that software and coding related courses will be included in the value added courses.
11.	2.3	Discussions on the course content of III, IV & V semester subjects and modifications are to be made.	The modifications in the III, IV & V semester subjects will be noted and are to be made.
12.	1.10	Considering job opportunities, syllabus is to be revised and is to be framed accordingly.	The AR17 syllabus will be revised and updated accordingly.
13.	1.3	The courses are to be ratified and should be focused on skill development/placements.	More focus will be indulge and the ratified courses on employability / entrepreneurship / skill development will be included in the AR 17 syllabus
14.	1.4	A choice based credit system is to be given to the students, so that students can choose their interested subject.	Necessary measures will be made for the students in such a way that choice based credit system will be implemented among the students.
15.	1.5	Students are suggested to change the pattern of the question paper.	Taking students feed back into considerations, essential arrangements will be made in the question paper pattern.
16.	1.8	Students are requested to remove the dress code and also requested to include skill courses.	A discussion will be conducted and the necessary actions will be taken on the dress code and also to include skill courses in the curriculum.



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